

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R22 REGULATION REGULAR EXAMINATIONS FEBRUARY -2024

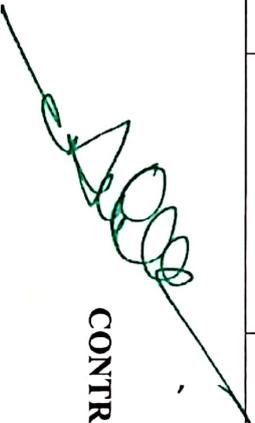
T I M E T A B L E

TIME → AN: 2:00 PM TO 5:00 PM

BRANCH	DATE, SESSION AND DAY						
	12-02-2024 MONDAY	14-02-2024 WEDNESDAY	16-02-2024 FRIDAY	19-02-2024 MONDAY	21-02-2024 WEDNESDAY	23-02-2024 FRIDAY	
COMPUTER SCIENCE & ENGINEERING (05- CSE)	Digital Electronics	Data Structures	Computer Oriented Statistical Methods	---	Object Oriented Programming through Java	Computer Organization and Architecture	
INFORMATION TECHNOLOGY (12-IT)	Digital Electronics	Data Structures	Computer Oriented Statistical Methods	Computer Organization and Microprocessor	Introduction to IoT	---	
COMPUTER SCIENCE AND BUSINESS SYSTEMS (32-CSBS)	Digital Electronics	Data Structures	Computer Oriented Statistical Methods	----	Object Oriented Programming through Java	Computer Organization and Architecture	

DATE: 30-01-2024

Sd/-
CONTROLLER OF EXAMINATIONS



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 EXAMINATION BRANCH
 II YEAR B.TECH - I SEMESTER - R22 REGULATION REGULAR EXAMINATIONS FEBRUARY -2024
 TIME TABLE

TIME → FN: 10:00 AM TO 1:00 PM

BRANCH	DATE, SESSION AND DAY					
	12-02-2024 MONDAY	14-02-2024 WEDNESDAY	16-02-2024 FRIDAY	19-02-2024 MONDAY	21-02-2024 WEDNESDAY	23-02-2024 FRIDAY
CIVIL ENGINEERING (01-C-E)	Probability and Statistics	Building Materials, Construction and Planning	Engineering Geology	Strength of Materials - I	Fluid Mechanics	--
ELECTRICAL AND ELECTRONICS ENGINEERING (02-EEE)	Electrical Machines-I	Analog Electronic Circuits	--	Power System-I	Electro Magnetic Fields	Numerical Methods and Complex variables
MECHANICAL ENGINEERING (03-ME)	Probability, Statistics & Complex Variables	Mechanics of Solids	Metallurgy & Material Science	Production Technology	Thermodynamics	--
ELECTRONICS & COMMUNICATIONS ENGINEERING (04-ECE)	--	Analog Circuits	Network analysis and Synthesis	Digital Logic Design	Signals and Systems	Numerical Methods And Complex Variables

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